
IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Jordan Plofsky

Attorney Docket No.: ALTRP082/A820

Application No.: 10/629,508

Examiner: Igbal, Nadeem

Filed: July 29, 2003

Group: 2114

Title: EMBEDDED MICROPROCESSOR FOR
INTEGRATED CIRCUIT TESTING AND
DEBUGGING

Confirmation No.: 6910

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I hereby certify that this correspondence is being transmitted electronically
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VA 22313-1450 on December 30, 2008.

Signed: _____/Danielle Jones/_____
Danielle Jones

**INFORMATION DISCLOSURE STATEMENT
37 CFR §§1.56 AND 1.97(b)**

Mail Stop RCE
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

The reference(s) identified in the attached PTO Form 1449 (copies of non-U.S. references are attached) may be material to examination of the above-identified patent application. Applicants identify the reference(s) in compliance with their duty of disclosure pursuant to 37 CFR §§1.56 and 1.97. The Examiner is requested to make the identified reference(s) of official record in this application.

This Information Disclosure Statement is not to be construed as a representation that a search has been made, that additional information material to the examination of this application does not exist, or that the identified reference(s) indeed constitute prior art.

This Information Disclosure Statement is: (i) filed within three (3) months of the filing date of the above-referenced application, (ii) believed to be filed before the mailing date of a first Office Action on the merits, or (iii) believed to be filed before the mailing of a first Office Action after the filing of a Request for Continued Examination under §1.114. Accordingly, it is believed that no fees are due in connection with the filing of this Information Disclosure

Statement. However, if it is determined that any fees are due, the Commissioner is hereby authorized to charge such fees to Deposit Account 504480 (Order No. ALTRP082).

Respectfully submitted,
Weaver Austin Villeneuve & Sampson LLP

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